

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-21. (Canceled)

22. (Currently Amended) A semiconductor device for driving a display section, the semiconductor device comprising:

a first input terminal to which compressed data is input;

a decoder which decompresses the compressed data;

first and second frame buffers each of which stores the decoded data for one frame by the decoder;

a RAM which stores the decoded data read out from one of the first and second frame buffers a decoded data by the decoder;

a driving section which is connected to an electrode of the display section; and

a controller which controls a decode timing of the decoder, a read timing from the RAM and the driving section,

wherein the controller causes the decoded data to be written into one of the first and second frame buffers from which decoded data is not output to the RAM; and

wherein a write speed of the decoded data for one frame into the RAM is higher than the read speed of the display data for one frame from the RAM.

23. (Previously Presented) The semiconductor device according to claim 22, further comprising:

a second input terminal to which text data is input, wherein the decoded data or the text data is written into the RAM.

24. (Currently Amended) A semiconductor device for driving a display section, the semiconductor device comprising:

an input terminal to which compressed data is input;

a decoder which decompresses the compressed data;

first and second frame buffers each of which stores the decoded data for one frame by the decoder;

a RAM which stores the decoded data read out from one of the first and second frame buffers a decoded data by the decoder;

a driver which is connected to an electrode of the display section; and

a controller which controls a decode timing of the decoder, a read timing from the RAM and the driver,

wherein the controller causes the decoded data to be written into one of the first and second frame buffers from which decoded data is not output to the RAM;

wherein the same decoded data is read out from the RAM at least two times, which the decoded data is decoded for one frame and written into the RAM; and

wherein a write speed of the decoded data for one frame into the RAM is higher than the read speed of the display data for one frame from the RAM.